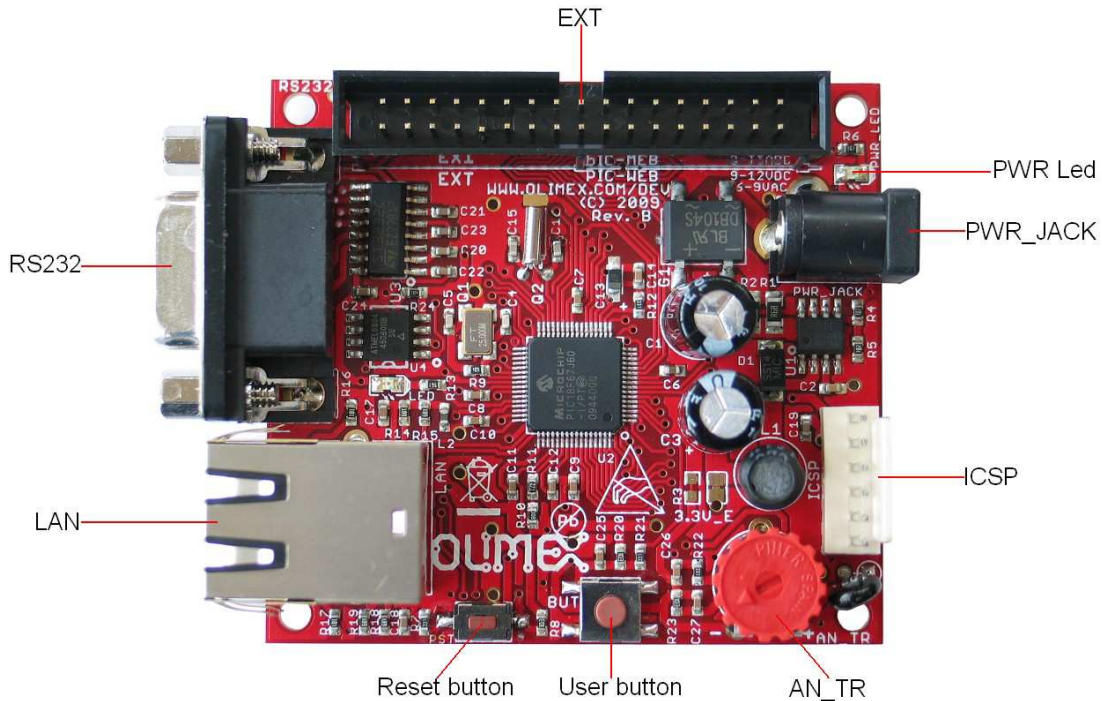


BOARD LAYOUT



POWER SUPPLY CIRCUIT

PIC-WEB can take power from two sources:

- PWR_JACK where (9-12) VDC is applied by external power source.
- EXT-20 pin VIN with the same voltage range.

The board power consumption is: about 130 mA with all peripherals and MCU running at full speed.

RESET CIRCUIT

PIC-WEB reset circuit is made with R8 (10k) pull-up, R7 (330R) serial resistor and RST button.

Although on the schematic is made provision for external reset through EXT-16 pin. Manual reset is possible by the RST button.

CLOCK CIRCUIT

Quartz crystal 25 MHz is connected to PIC18F67J60 clock in and clock out.

Quartz crystal 32.768 KHz is connected to PIC18F67J60 T1OSO and T1OSI pins for it's internal Real Time Clock.

JUMPER DESCRIPTION

3.3V_E



This jumper, when closed, enables 3.3V board power supply.
Default state is closed.

INPUT/OUTPUT

One User button with name **BUT** – connected to PIC18F67J60 pin 3 (RB0/INT0/FLT0);

Status red LED with name **LED** connected to PIC18F67J60 pin 44 (RB4/KBI0).

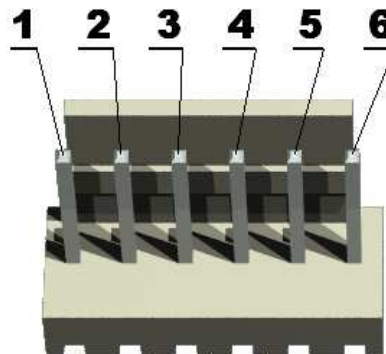
Power supply red LED with name **PWR** – indicates that external power source is applied and board power supply is applied.

One trimmer **AN_TR** is connected to PIC18F67J60 pin 15 (RF3/AN8).

EXTERNAL CONNECTORS DESCRIPTION

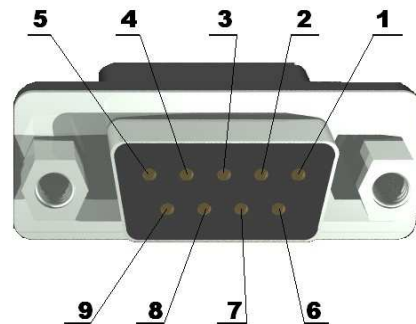
ICSP

Pin #	Signal Name
1	RST
2	+5V
3	GND
4	PGD
5	PGC
6	PGM - NC



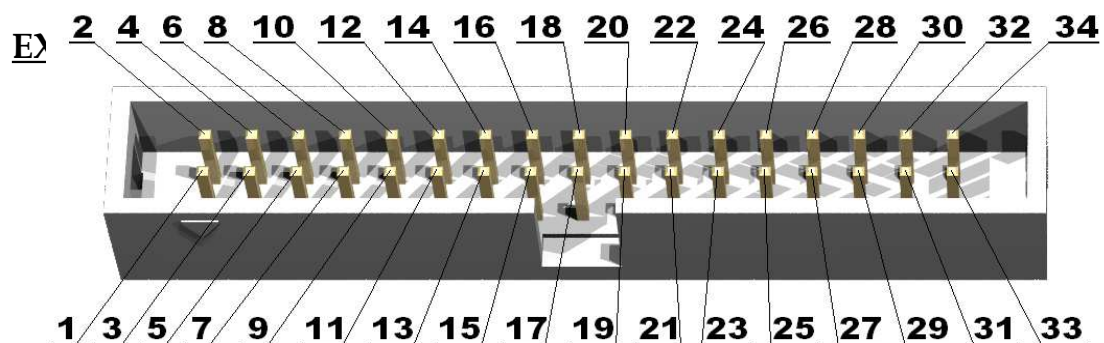
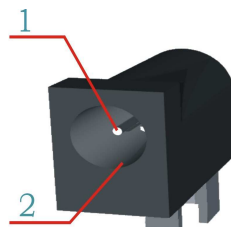
RS232

Pin #	Signal Name
1	NC
2	T1OUT
3	R1IN
4	NC
5	GND
6	NC
7	R2IN
8	T2OUT
9	NC



PWR JACK

Pin #	Signal Name
1	Power Input
2	GND



Pos & Neg might be changed
0

Pin #	Signal Name	Pin #	Signal Name
1	RA2/AN2/VREF-	2 <i>M26p</i>	RA3/AN3/VREF+
3	RA4/T0CKI	4 <i>M16p</i>	RA5/AN4
5 <i>PLP1</i>	RE0/P2D	6 <i>PLP2</i>	RE1/P2C
7 <i>PLP3</i>	RE2/P2B	8 <i>M36p</i>	RC2/ECCP1/P1A
9 <i>M2an</i>	RD0/P1B	10	RD1/ECCP3/P3A
11 <i>M3ap</i>	RD2/CCP4/P3D	12 <i>M36n</i>	RB1/INT1
13 <i>M3an</i>	RB2/INT2	14 <i>M2ap</i>	RB3/INT3
15	RB5/KBI1	16	RST
17	+3,3V	18	+3.3V
19	GND	20	VIN
21 <i>PLP4</i>	RE3/P3C	22 <i>PLP6</i>	RE4/P3B
23 <i>M1ap</i>	RE5/P1C	24 <i>M2an</i>	RF1/AN6/C2OUT
25 <i>M16n</i>	RF2/AN7/C1OUT	26 <i>M26n</i>	RF5/AN10/CVREF
27	RF6/AN11	28 <i>PLP5</i>	RG4/CCP5/P1D
29	CTS	30	RTS
31	<i>RAI</i> NC	32	NC
33	3.3VA	34	GNDA

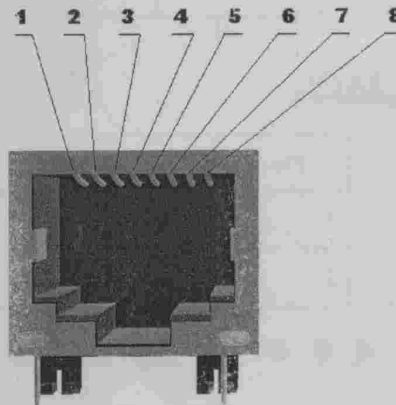
LAN

PLP pin

- 2
- 3
- 5
- 6
- 7

- PLP 4*
- PLP 2*
- 3
- 4
- 5

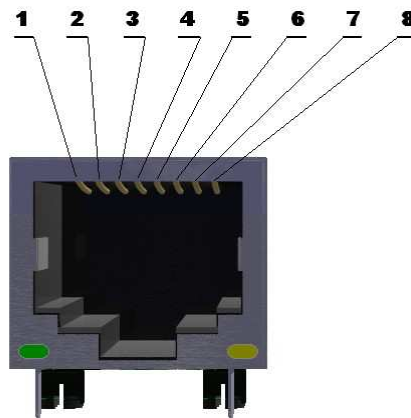
Pin #	Signal Name
1	TPOUT+
2	TPOUT-
3	3.3v
4	NC
5	NC
6	NC



Pin #	Signal Name	Pin #	Signal Name
1	RA2/AN2/VREF-	2	RA3/AN3/VREF+
3	RA4/T0CKI	4	RA5/AN4
5	RE0/P2D	6	RE1/P2C
7	RE2/P2B	8	RC2/ECCP1/P1A
9	RD0/P1B	10	RD1/ECCP3/P3A
11	RD2/CCP4/P3D	12	RB1/INT1
13	RB2/INT2	14	RB3/INT3
15	RB5/KBI1	16	RST
17	+3,3V	18	+3.3V
19	GND	20	VIN
21	RE3/P3C	22	RE4/P3B
23	RE5/P1C	24	RF1/AN6/C2OUT
25	RF2/AN7/C1OUT	26	RF5/AN10/CVREF
27	RF6/AN11	28	RG4/CCP5/P1D
29	CTS	30	RTS
31	NC	32	NC
33	3.3VA	34	GNDA

LAN

Pin #	Signal Name
1	TPOUT+
2	TPOUT-
3	3.3v
4	NC
5	NC
6	NC



7	TPIN+
8	TPIN-

LED	Color	Usage
Right	Yellow	Activity
Left	Green	100MBits/s (Half/Full duplex)

MECHANICAL DIMENSIONS

